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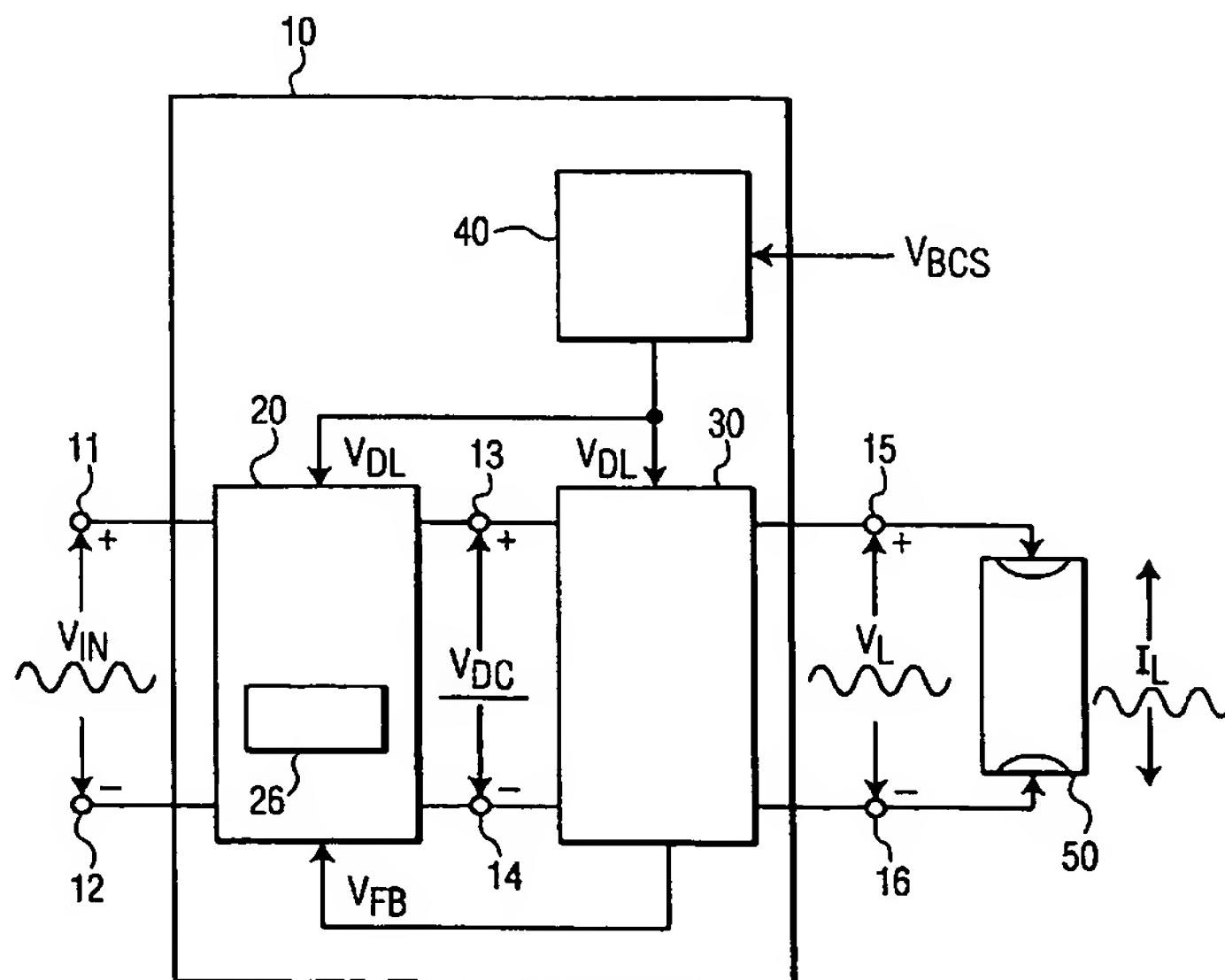
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- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE,

[Continued on next page]

(54) Title: TOTAL HARMONIC DISTORTION REDUCTION FOR ELECTRONIC DIMMING BALLAST



(57) Abstract: A ballast employs an inventor output stage and a power factor correction input stage, which applies a regulated DC voltage as a function of a line voltage to the inventor output stage. The power factor correction input stage includes a power factor correction integrated circuit and a line voltage sensing circuit, which applies a clamped rectified voltage to the power factor correction integrated circuit. The clamped rectified voltage is a function of a load being applied by the inventor output stage to the power factor correction integrated circuit.

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